IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Applicant: Gauthier et al.

App. No.: 10/771,989

Filed:

February 4, 2004

Title:

COMPENSATION TECHNIQUE TO MITIGATE AGING EFFECTS IN

INTEGRATED CIRCUIT

COMPONENTS

Patent No.: 7,129,800 B2

Issue Date: October 31, 2006

Con. No.:

3585

Art Unit:

2817

Examiner:

Kinkead, Arnold M.

REQUEST FOR CERTIFICATE OF CORRECTION UNDER 37 C.F.R. § 1.322

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicant hereby requests a Certificate of Correction under 37 C.F.R. § 1.322 be issued for the above patent, in accordance with the attached request.

The Patent Office has misspelled the word "loop" in line 5 of claim 10.

All errors sought to be corrected were made by the Patent Office. Therefore, the Assignee believes no fee is due with this filing. However, should any fees or petitions be required, please consider this a request therefor and authorization to charge Deposit Account No. 04-1415 as necessary.

Should the Examiner have any questions, please contact the undersigned attorney.

Dated: 10, JAN 2007

Respectfully submitted,

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CO

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO.

7,129,800 B2

ISSUE DATE

October 31, 2006

INVENTOR(S)

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It is certified that an error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 10, at line 28, delete --ioop-- and insert "loop".

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